

To: Technology Center: 2128  
Facsimile Number: 571-273-8300

Total Pages Sent: 16

From: Robert D. Marshall, Jr.  
Texas Instruments Incorporated  
Facsimile: 972-917-4418  
Phone: 972-917-5290

RECEIVED  
CENTRAL FAX CENTER

DEC 09 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

TI-30481

Gary L. Swoboda, et al.

Art Unit: 2128

Serial No.: 09/943,599

Examiner: Akash Saxena


Filed: August 30, 2001

Conf. No.: 2479

For: Correlating On-Chip Data Processor Trace Information for Export

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 571-273-8300 on the date shown below:

  
Robin E. Barnum

December 9, 2005  
Date

FACSIMILE COVER SHEET

<input checked="" type="checkbox"/> <b>FACSIMILE COVER SHEET</b>	<input checked="" type="checkbox"/> <b>AMENDMENT</b> <u>116</u> (15 Pages)
<input type="checkbox"/> <b>NEW APPLICATION</b>	<input type="checkbox"/> <b>EOT</b> _____ (# Page)
<input type="checkbox"/> <b>DECLARATION (# Pages)</b>	<input type="checkbox"/> <b>NOTICE OF APPEAL (# Pages)</b>
<input type="checkbox"/> <b>ASSIGNMENT (# Pages)</b>	<input type="checkbox"/> <b>APPEAL</b> _____ (# Pages)
<input type="checkbox"/> <b>FORMAL DRAWINGS</b>	<input type="checkbox"/> <b>ISSUE FEE (# Pages)</b>
<input type="checkbox"/> <b>INFORMAL DRAWINGS</b>	<input type="checkbox"/> <b>REPLY BRIEF (IN TRIPLICATE) (# Pages)</b>
<input type="checkbox"/> <b>CONTINUATION APP'N (# Pages)</b>	
<input type="checkbox"/> <b>DIVISIONAL APP'N</b>	
<b>NAME OF INVENTOR(S):</b> Gary L. Swoboda, et al.	
<b>RECEIPT DATE &amp; SERIAL NO.:</b> <b>Serial No.: 09/943,599</b> <b>Filing Date: August 30, 2001</b>	
<b>TITLE OF INVENTION:</b> Correlating On-Chip Data Processor Trace Information for Export	
<b>TI FILE NO.:</b> <b>TI-30481</b>	<b>DEPOSIT ACCT. NO.:</b> <b>20-0668</b>
<b>FAXED:</b> 12/9/05 <b>DUE:</b> 12/9/05 <b>ATTY/SEC'Y:</b> RDM/reb	

This facsimile is intended only for the use of the address named and contains legally privileged and/or confidential information. If you are not the intended recipient of this telecopy, you are hereby notified that any dissemination, distribution, copying or use of this communication is strictly prohibited. Applicable privileges are not waived by virtue of the document having been transmitted by Facsimile. Any misdirected facsimiles should be returned to the sender by mail at the address indicated on this cover sheet.

Texas Instruments Incorporated  
PO Box 655474, M/S 3999  
Dallas, TX 75265

DEC 09 2005

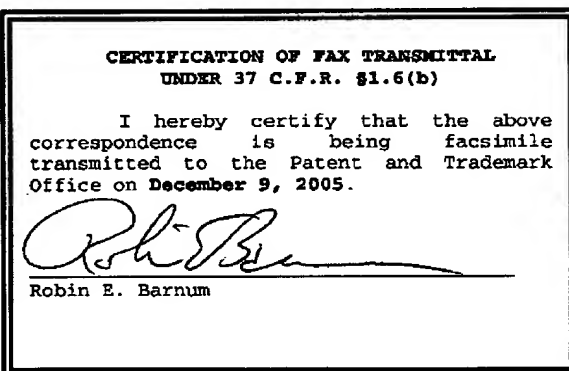
RESPONSE UNDER 37 CFR 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2128

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Swoboda et al                      Art Unit: 2128  
Serial No.: 09/943,599                      Examiner: Akash Saxena  
Filed: August 30, 2001                      Docket: TI-30481  
For: CORRELATING ON-CHIP DATA PROCESSOR TRACE INFORMATION FOR  
EXPORT

AMENDMENT UNDER 37 C.F.R. 1.116

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450



Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated September 9, 2005. These amendments are respectfully submitted as a full and complete response to that Action.

Please amend the above referenced application as follows: